| Notice of Allowability | Application No. | Applicant(s) |
|--|----------------------------|-----------------------------|
| | 09/600,890 | SHIRAI ET AL. |
| | Examiner | Art Unit |
| | Tuan T. Dinh | 2841 |
| The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. | | |
| 1. This communication is responsive to <u>09/08/05</u> . | | |
| 2. ☑ The allowed claim(s) is/are 1-3,5-10,12,14-17,19,21-39 7, 29 - 3-6 | | |
| 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) ☑ All b) ☐ Some* c) ☐ None of the: 1. ☑ Certified copies of the priority documents have been received. 2. ☐ Certified copies of the priority documents have been received in Application No 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). * Certified copies not received: | | |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. | | |
| 4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. | | |
| 5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. | | |
| (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached | | |
| 1) hereto or 2) to Paper No./Mail Date | | |
| (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date | | |
| Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). | | |
| DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. | | |
| | | |
| Attachment(s) 1. ☐ Notice of References Cited (PTO-892) | | |
| 2. ☐ Notice of Praftperson's Patent Drawing Review (PTO-948) | <u> </u> | atent Application (PTO-152) |
| • | 6. | (P1O-413), e |
| Information Disclosure Statements (PTO-1449 or PTO/SB/0. Paper No./Mail Date 09/08/05 | 8), 7. 🗌 Examiner's Amendn | nent/Comment |
| 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material | 8. | nt of Reasons for Allowance |
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DETAILED ACTION

The request filed on 09/08/05 for a Request for Continued Examination (RCE) under 37 CFR 1.114 based on parent Application No. 09/600,890 is acceptable and a RCE has been established. An action on the RCE follows.

Allowable Subject Matter

1. Claims 1-3,5-10,12,14-17,19,21-27, and 29-36 are allowed.

The following is an examiner's statement of reasons for allowance: the references cited disclose a multiplayer printed circuit board comprising conductor circuit layers, interlaminate insulative resin layers, through holes each having an inner wall, and some other claimed elements, for example, Uno et al. (US. Patent 5,827,604) discloses a multiplayer printed circuit board comprising conductor layers (3) each having a thickness, and interlaminate insulative resin layers (4) each having a through hole (7 or 11) including an inner wall, the inner wall filled up with a plating layer.

Frankeny et al. (US Patent 5,509,200) discloses a multiplayer printed circuit board as shown in figure 9 comprising dendrites (21) filled in inner walls of through holes (13). However, they do not disclose or render obvious in combination of the through holes, each having the inner wall filled up with the plating layer having a substantially flat surface to form a via hole, and the surface of the plating layer that extends out of the through hole and lies in a substantially same level as the surface of the conductor circuit layer (as recited in claim 1), and depressions are formed on a central surface portion of

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the plating layer extending out of the through hole (as recited in claims 9, 17, and 25).

Also, there is no suggestion to modify these limitations as claimed as above.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T Dinh whose telephone number is 571-272-1929. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammie Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan Dinh September 12, 2005. NAMAND CUNED
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800

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